

## Loris Giuseppe Navoni

via Piave 10  
20063 Cernusco s/N  
tel. 02-9231866  
mobile 333 7496273  
e-mail : [maganilo@fastwebnet.it](mailto:maganilo@fastwebnet.it)  
web: <http://digilander.libero.it/navoni>  
[www.linkedin.com/in/lorisnavoni](http://www.linkedin.com/in/lorisnavoni)

### summary

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Current work: Content management system for Design for Manufacturing.  
Recent work: System Level Design field ,focusing on memory card solutions , as well as the study of right Design methodologies for SoC.  
Past: design of speech-related applications for portable devices, ( Palm OS-based, customs ), and other applications for demonstrative purpose on prototypes of new devices.  
Holds US and EU patents

### experience

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## STMicroelectronics srl , Agrate Brianza

1998 - current

**in the Innovative System Design Team as Senior System Designer**

Current activities:

- **Information management for IC verification:**
  - Content Management System for DFM : automatic reporting
  - Data management for Design Rules manual implementation
- **Design for Manufacturing:** tools and methodologies

Past Projects and Activities

- **Project Leader** in: Analysis and design of the security part of a **Secure MMC card**, implementing several protocols ( **MMC 4.1**, **SecureMMC 2.0**, **OMA-DRM** ) ( lead 1 people )
- **Project Leader in:** Design of a **Secure Digital memory card** controller ( full custom ASIC) (lead 2 people)
- **Project Leader** in: Implementation of an **Human interface** for **Palm OS** computer with a System-On-Chip **speech recognition engine**
- Development of User Interfaces and Applications for IC prototypes

1992 – 1997

**in the Innovative System Design Group as SW/CAD Designer**

Applications:

- **OCR** using associative memory device: demonstrative program;
- **JPEG Image Compressor** on ST20 microprocessor;
- Audio/Video demonstration for multilevel FLASH memory.
- Image Compression using Vector Quantization on Assoc.Memory

Document analysis:

- Demos on Word Recognition and Printed Character Recognition using Associative Memory device;

Development of a toolkit for analog CAD, based on Cadence platform.

1987 – 1992

**in the CAD & LIBRARY DEVELOPMENT GROUP as CAD designer**

- Analysis, development and maintenance of several CAD applications

## Norditalia Assicurazioni, Milano

1983 – 1987

Technical assistance for agencies in national territory in installing PC, support software, training and hot line.

## Known systems, languages and packages

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<b>Programming Languages</b>	Good understanding of <b>C/C++</b> , <b>Java</b> , competence on <b>Perl</b> , TCL/TK, <b>UML</b> ,
<b>Dedicated Prog. Languages</b>	Good understanding in some languages related to <b>IC</b> and <b>System-on-Chip</b> design: <b>VHDL</b> , <b>Esterel</b> , <b>SystemC</b> , Cadence <b>Skill</b>
<b>Mark-up Languages</b>	Understanding in <b>HTML</b> , <b>XML</b> , XSL
<b>Operating Systems</b>	As server OS: <b>UNIX (Linux, Solaris)</b> As development machine: <b>MS Windows (XP, 2000, NT 4)</b> , <b>Linux (RedHat)</b> and <b>PalmOS 3.5</b>
Fluent use of the most common Text Editing, and MS software packages.	
<b>Others:</b>	Experience on using many EDA tools, Oscilloscope, logic state analyzer

## Conference, Papers, Patents

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### As main author:

- "**An Analog Associative Memory Chip for VQ Image Compression**", ICASSP '99, Phoenix, Arizona, March 1999
- "**Words Recognition using Associative Memory**", ICDAR'97, Ulm, Germany, August 1997
- "**A way to integrate, manage and use module generators**", CAVE 1992, La Toya, Spain
- "**Unigen: an application shell for module generators**", CAVE 1989, Nice, France

### Several other papers as co-author

Holds 7 granted US patent(s) and 1 granted European patent(s).

## Personal Data

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**Birth, City & Date** Milano, June 27, 1963  
**Status** married, 3 children  
**Foreign Language:** English

## Education

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Senior High School Diploma: Technical Computer Science  
I.T.S.O.S. "Marie Curie" Cernusco s/N  
Marks: 58/60 Year: June 1982

## Training

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- "Esterel Studio", December 2002, UTC, Grenoble
- "**PalmOS** Programming Essentials", February 2000, Symbol University, Reading, UK;
- "Programmazione **Java**", April 1997, Sun Microsystems, Agrate;
- "Preview with BlockEnsemble P&R ", March 1995, SGS-Thomson Training Center, Grenoble;
- "CMOS & BiCMOS **VLSI Design** '93: introduction to digital integrated circuit design", August 1993, EPFL, Losanna;
- "**Reti neurali**, principi di base", November 1992, Systech, Milano;
- "Synthesis", may 1992, SGS-Thomson Training Center, Grenoble;
- "VHSIC HW Description Language", may 1992, SGS-Thomson Training Center, Grenoble;
- "Tecniche di presentazione"- "Project Management"- "Latch cascade TQM training ", SGS-Thomson internal course, Agrate

## Others

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- Available for short trips in Italy and worldwide
- Find me at **www.linkedin.com**
- Interests: mountains, reading, writing
- Published a book of stories  
( **Alta Via**, ed. Lampi di Stampa, May 2005 )

**Autorizzo al trattamento dei dati personali ai sensi del D.L. 196/2003.**